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				Application Number	10/774,815			
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eet	1	of	2	Attorney Docket Number	800.0133			

U.S. PATENT DOCUMENTS								
Examiner Initials*	Cite No.	Document Number  Number - Kind Code <sup>2 (M known)</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear			
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Examiner	١	Foreign Patent Document				
Initials*	Cite No.1	Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known) EP :- 0569763	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	₩
			11/18/1993	International Business Machines Corp.		
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NON PATENT LITERATURE DOCUMENTS							
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	_	PECHANEK ET AL., M.F.A.S.T.: A Single Chip Highly Parallel Image Processing Architecture, Proceedings of the International Conference on Image Processing (ICIP), 19/23/1995, Page(s) 69-72, Volume 3, Publisher: IEEE Comp. Soc. Press					
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